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| Notice of References Cited | Application/Control No. 09/802,952 | Applicant(s)/Patent Under Reexamination YAMASHITA ET AL. | |
| | Examiner Amol V. Gole | Art Unit 2183 | Page 1 of 1 |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|-------------------|----------------|
| | A | US-5,506,976 A | 04-1996 | Jaggar, David V. | 712/238 |
| | B | US-6,304,962 B1 | 10-2001 | Nair, Ravindra K. | 712/240 |
| | C | US-5,898,866 A | 04-1999 | Atkins et al. | 712/241 |
| | D | US- | | | |
| | E | US- | | | |
| | F | US- | | | |
| | G | US- | | | |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|---|
| | U | Breeding, "Microprocessor System Design Fundamentals," Prentice Hall, 1995 |
| | V | |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.